Attorney Docket No. ALT.P022 (A980)   **Patent**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:) Examiner: Garbowski, Leigh M.
)
    Deshanand P. Singh, et al.   ) Art Unit No. 2825
)
Application No.: 10/617,502   )
)
Filed: July 11, 2003   )
)
For: METHOD AND APPARATUS FOR   )
        PERFORMING INCREMENTAL   )
        PLACEMENT FOR LAYOUT-DRIVEN   )
        OPTIMIZATIONS ON FIELD   )
        PROGRAMMABLE GATE ARRAYS   )
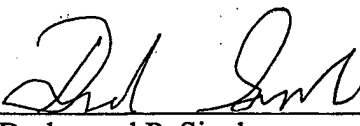_____   )

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

DECLARATION UNDER RULE 132

1. My name is Deshanand P. Singh. I am over the age of 21 years, of sound mind, and

   competent in all respects to make this Declaration. I am a Supervising Principal

   Engineer at Altera Corporation. My business address is 151 Bloor Street West, Suite

   200, Toronto, Ontario, Canada M5S 1S4.

2. I have co-authored a publication with Stephen D. Brown entitled "Incremental

   Placement for Layout-Driven Optimizations on FPGAs", published in Computer

   Aided Design, ICCAD 2002. IEEE/ACM International, on November 2002

   (hereinafter "Publication"). I have read and understand the Publication.

3. I am a co-inventor of the above referenced patent application. I have read and understand the claims in the above referenced patent application.

4. I understand that the Publication has been cited against the above referenced patent application. To the extent that the described subject matter in the Publication relates to the claimed invention in the above referenced patent application, Stephen D. Brown and I are co-inventors of the described subject matter in the Publication.

5. I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application and any patent issued thereon.

Dec 16, 2005
Date

Deshanand P. Singh